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09417332 - GAIU 2183
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(Not for submission under 37 CFR 1.99)

Application Number	09417332
Filing Date	1999-10-13
First Named Inventor	Robert Bedichek
Art Unit	2183
Examiner Name	Ellis, Richard L.
Attorney Docket Number	TRAN-P009

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	1	000882042	EP			Sun Microsystems, Inc.		<input type="checkbox"/>
	2	0118665	WO			Transmeta Corporation		<input type="checkbox"/>

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	1	Bala et al., "TRANSPARENT DYNAMIC OPTIMIZATION: THE DESIGN AND IMPLEMENTATION OF DYNAMO;" HP Laboratories Cambridge HPL-1999-78; June 1999 (Tran291)	<input type="checkbox"/>
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	7	Non Final Office Action; Mail Date 03/29/2006; Serial Number 10/406,022.	<input type="checkbox"/>
	8	Notice of Allowance; Mail Date 02/21/2003; Serial Number 09/539,987.	<input type="checkbox"/>
	9	Examiner Interview Summary; Mail Date 04/14/2003; Serial Number 09/417,358.	<input type="checkbox"/>
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	12	Notice of Allowance; Mail Date 06/20/2003; Serial Number 09/417,358.	<input type="checkbox"/>
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